# Finding operating points in networks containing MOS transistors by a piecewise-linear approach 

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#### Abstract

In this paper we present a methodology for finding operating points in networks containing MOS transistors, linear positive resistors, and independent voltage and current sources. The MOS transistors are described by the high-canonical piecewise-linear (HC-PWL) model. A hybrid formulation is obtained from the network under analysis and it is solved in order to find the solution(s). The methodology takes advantage from the uniformly spaced simplicial partition in which the HC-PWL model is based because it makes possible to apply the Kuh-Chien algorithm. This algorithm has proved its efficiency to solve nonlinear resistive networks into simplicial subdivision schemes.


Keywords-DC solutions, Simplicial partition, High canonical piecewise-linear, MOS transistor networks

## I INTRODUCTION

The task of finding the operating points, in networks containing elements which are described by piecewise-linear (PWL) models, is a topic of interest in nonlinear circuit theory. Although numerous important references about PWL modeling and analysis can be found [1]- [4], there are not reported techniques which deal with the problem of applying the HC-PWL to DC analysis, and specifically with the task of computing operating points. This paper intends to be a first draft to overcome such problem. The main contribution of this paper is a methodology which is based on the Kuh-Chien algorithm [9], for computing operating points by a PWL approach where the nonlinear elements are described by the HC-PWL model. The network under study is considered to include MOS transistors which are described by the two dimensional HCPWL model reported by Julián "et al." in references [5] and [6]. Such model describes a $n$-dimensional PWL function $f(\mathbf{x})$ by the analytical expression

$$
f(\mathbf{x})=\mathbf{C}^{T} \boldsymbol{\Lambda}(\mathbf{x}), \forall \mathbf{x} \in S
$$

Defined over a rectangular compact domain $S$ in $R^{n}$ $S=\left\{\left(x_{1}, \cdots, x_{n}\right): 0 \leq x_{i} \leq m_{i} \delta, \quad i \in\{1, \cdots, n\}\right\}$
when $\delta$ is a parameter called the grid step and $m_{i}$ defines the rectangular length which is simplex partitioned.
$\mathbf{C}$ is denoted as vector of parameters and $\boldsymbol{\Lambda}$ is an expression which contains terms described by the so called absolute value $\gamma$ function that is given by

$$
\begin{aligned}
\gamma\left(f_{i}, f_{j}\right)= & \frac{1}{4}\left\{\left|\left|-f_{i}\right|+f_{j}\right|-\left|-f_{i}+\left|f_{j}\right|\right|\right\} \\
& +\frac{1}{4}\left\{\left|-f_{i}\right|+\left|f_{j}\right|-\left|-f_{i}+f_{j}\right|\right\}
\end{aligned}
$$

with $f_{i}$ and $f_{j}$ as hyperplane equations.
A more detailed explanation about the HC-PWL model can be found in references [5], [6], and [7]. In this paper we are specifically interested in a methodology for computing operating points that is compatible with the HC-PWL model. The model description of the MOS transistors is assumed to be the explicit form of the HC-PWL representation reported in [7].

## II MOS TRANSISTOR NETWORK

Fig. 1 shows a $2 \mu$-port network terminated by $\mu$ MOS transistors. In the network there are linear positive resistors and independent voltage and current sources.


Figure 1: A $2 \mu$ port terminated by MOS transistors.

A hybrid formulation for the $2 \mu$-port is reported in reference [8] and it is given by

$$
\left[\begin{array}{c}
\mathbf{i}_{\alpha}  \tag{1}\\
\mathbf{v}_{\beta}
\end{array}\right]=-\left[\begin{array}{ll}
\mathbf{H}_{\alpha \alpha} & \mathbf{H}_{\alpha \beta} \\
\mathbf{H}_{\beta \alpha} & \mathbf{H}_{\beta \beta}
\end{array}\right]\left[\begin{array}{c}
\mathbf{v}_{\alpha} \\
\mathbf{i}_{\beta}
\end{array}\right]+\left[\begin{array}{c}
\mathbf{d}_{\alpha} \\
\mathbf{d}_{\beta}
\end{array}\right]
$$

where

$$
\begin{gathered}
\mathbf{i}_{\alpha}=\left[i_{D S 1} \cdots i_{D S(2 \mu-1)}\right]^{T}, \mathbf{i}_{\beta}=\left[i_{G S 2} \cdots i_{G S(2 \mu)}\right]^{T}, \\
\mathbf{v}_{\alpha}=\left[v_{D S 1} \cdots v_{D S(2 \mu-1)}\right]^{T}, \mathbf{v}_{\beta}=\left[v_{G S 2} \cdots v_{G S}(2 \mu)\right]^{T}
\end{gathered}
$$

are port currents and port voltages, $\mathbf{d}_{\alpha}, \mathbf{d}_{\beta}$ are subvectors of source vector.
From the MOS physics characteristics it can be seen that $\mathbf{i}_{\alpha}=\mathbf{f}\left(\mathbf{v}_{\alpha}, \mathbf{v}_{\beta}\right)$ and $\mathbf{i}_{\beta}=\mathbf{0}$. So that the hybrid formulation is reduced to:

$$
\begin{align*}
\mathbf{f}\left(\mathbf{v}_{\alpha}, \mathbf{v}_{\beta}\right)+\mathbf{H}_{\alpha \alpha} \mathbf{v}_{\alpha} & =\mathbf{d}_{\alpha}  \tag{2}\\
\mathbf{H}_{\beta \alpha} \mathbf{v}_{\alpha}+\mathbf{v}_{\beta} & =\mathbf{d}_{\beta} \tag{3}
\end{align*}
$$

## III TWO DIMENSIONAL SIMPLICIAL SUBDIVISION

In the network shown in Fig.1, it is considered that the functions $i_{\alpha}=f\left(v_{\alpha}, v_{\beta}\right)$ are defined over a simplicial partition space. Hence, it is important to introduce the notation that here after will be used to refer the simplices. Figure 2 shows a two-dimensional space which has been simplically partitioned.


Figure 2: A two-dimensional simplicial partition.
Let $\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}$ be three points in this space.
A simplex $S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ is defined by

$$
\begin{equation*}
S=\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)=\left\{\mathbf{x}: \mathbf{x}=\sum_{i=0}^{2} \mu_{i} \mathbf{x}_{i}\right\} \tag{4}
\end{equation*}
$$

with $1 \geq \mu_{i} \geq 0, i=\{0,1,2\}$
and $\sum_{i=0}^{2} \mu_{i}=1$
The points $\mathbf{x}_{0}, \mathbf{x}_{1}$, and $\mathbf{x}_{2}$ are called vertices of the simplex. Corresponding to three vertices, there are three boundaries $B_{k}$.
$B_{k}$ contains all the vertices except $\mathbf{x}_{k}$ and it is defined as

$$
\begin{equation*}
B_{k}=\left\{\mathbf{x}: \mathbf{x} \in S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)\right\} \tag{5}
\end{equation*}
$$

with $\mu_{k}=0$, and $k=\{0,1,2\}$
The intersection of two boundaries is called corner. Thus every vertex is in fact a corner. Fig. 3 depicts the geometrical relation between the vertices and boundaries for any simplex in a two-dimensional space.


Figure 3: $S=\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ and its boundaries.

## IV REPLACEMENT RULE

In reference [9], Kuh and Chien proposed a technique denominated replacement rule. It permits to trace a path in a structure of simplices. The technique considers that a new simplex can be reached by deleting a vertex and crossing its boundary. This idea is graphically depicted in Fig. 4.


Figure 4: Replacement rule.
Let the boundaries set be defined by

$$
B_{k}=\left\{\mathbf{x}:\left[\begin{array}{c}
\mathbf{x}  \tag{6}\\
1
\end{array}\right]=\left[\begin{array}{ccc}
\mathbf{x}_{0} & \mathbf{x}_{1} & \mathbf{x}_{2} \\
1 & 1 & 1
\end{array}\right]\right\} \mu
$$

where the $k$-th component of $\mu$ is zero, $\left(\mu_{k}=0\right)$
The new region is determined by the boundary $B_{k}$, and a new vertex $\hat{\mathbf{x}}_{k}$ is computed from

$$
\begin{equation*}
\hat{\mathbf{x}}=\mathbf{x}_{k+1}+\mathbf{x}_{k-1}-\mathbf{x}_{k} \tag{7}
\end{equation*}
$$

with $k=1$ at reference value

Equation (7) is usefull in the iterative process of finding an operating point, because it indicates how to traverse simplices until the solution is reached.

## V THE KUH-CHIEN ALGORITHM

Let the two-dimensional equation $\mathbf{Y}(\mathbf{x})=\mathbf{G}(\mathbf{x})$ be defined on a finite simplicial partition domain.
Let $S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ be any simplex in a simplicial partition. An affine function approximating the given $\mathbf{g}(\cdot)$ on $S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ can be defined by

$$
\begin{equation*}
\mathbf{Y}(\mathbf{x})=\left[\mathbf{g}\left(\mathbf{x}_{0}\right), \mathbf{g}\left(\mathbf{x}_{1}\right), \mathbf{g}\left(\mathbf{x}_{2}\right)\right] \mu \tag{8}
\end{equation*}
$$

for $x \in S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ and $\mu=\left[\mu_{0}, \mu_{1}, \mu_{2}\right]^{T}$
Let adopt the following representation:

$$
\left[\begin{array}{c}
\mathbf{Y}(\mathbf{x})  \tag{9}\\
1
\end{array}\right]=\left[\begin{array}{ccc}
\mathbf{g}\left(\mathbf{x}_{0}\right) & \mathbf{g}\left(\mathbf{x}_{1}\right) & \mathbf{g}\left(\mathbf{x}_{2}\right) \\
1 & 1 & 1
\end{array}\right] \mu
$$

for $\mathbf{x} \in S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ with

$$
\left[\begin{array}{c}
\mathbf{x}  \tag{10}\\
1
\end{array}\right]=\left[\begin{array}{ccc}
\mathbf{x}_{0} & \mathbf{x}_{1} & \mathbf{x}_{2} \\
1 & 1 & 1
\end{array}\right] \mu
$$

Then, equation (9) can be rewritten as

$$
\left[\begin{array}{c}
\mathbf{Y}(\mathbf{x})  \tag{11}\\
1
\end{array}\right]=\left[\begin{array}{ccc}
\mathbf{g}\left(\mathbf{x}_{0}\right) & \mathbf{g}\left(\mathbf{x}_{1}\right) & \mathbf{g}\left(\mathbf{x}_{2}\right) \\
1 & 1 & 1
\end{array}\right] \mathbf{X}_{\mu}
$$

with

$$
\mathbf{X}_{\mu}=\left[\begin{array}{ccc}
\mathbf{x}_{0} & \mathbf{x}_{1} & \mathbf{x}_{2}  \tag{12}\\
1 & 1 & 1
\end{array}\right]^{-1}\left[\begin{array}{c}
\mathbf{x} \\
1
\end{array}\right]
$$

Since $\mathbf{x} \in S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ if and only if the vector $\mu$ satisfies the condition $1 \geq \mu \geq 0$, then it is very easy to check whether an approximate solution of $\mathbf{Y}=\mathbf{G}(\mathbf{x})$, is found in $S\left(\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right)$ if and only if the solution of eq. (9) satisfies $\mu \geq \mathbf{0}$.

If there exists any negative element in $\mu$, then the solution must be reached in other simplex. It implies an iterative process of solving eq.(9) and checking $\mu$. Aided by the replacement rule, the new region which the solution enters is easily determined.

## VI METHODOLOGY

Let a nonlinear network containing linear resistors, independent voltage sources, independent current sources and MOS transistors be described by the HC-PWL model as a the analytical PWL function $i_{D S}=\mathbf{C}^{T} \boldsymbol{\Lambda}\left(v_{D S}, v_{G S}\right)$. The methodology for finding operating points is summarized in the following steps:

- Step 1: Obtain the reduced hybrid formulation for the network depicted in Fig. 1 as

$$
\begin{align*}
\mathbf{f}\left(\mathbf{v}_{\alpha}, \mathbf{v}_{\beta}\right)+\mathbf{H}_{\alpha \alpha} \mathbf{v}_{\alpha} & =\mathbf{d}_{\alpha}  \tag{13}\\
\mathbf{H}_{\beta \alpha} \mathbf{v}_{\alpha}+\mathbf{v}_{\beta} & =\mathbf{d}_{\beta} \tag{14}
\end{align*}
$$

where $\mathbf{f}\left(\mathbf{v}_{\alpha}, \mathbf{v}_{\beta}\right)$ has the form $\mathbf{C}^{T} \boldsymbol{\Lambda}\left(v_{D S}, v_{G S}\right)$

- Step 2: Recast the reduced hybrid formulation into the form $\mathbf{Y}=\mathbf{0}$ as

$$
\mathbf{Y}=\left[\begin{array}{c}
\mathbf{f}\left(\mathbf{v}_{\alpha}, \mathbf{v}_{\beta}\right)+\mathbf{H}_{\alpha \alpha} \mathbf{v}_{\alpha}-\mathbf{d}_{\alpha}  \tag{15}\\
\mathbf{H}_{\beta \alpha} \mathbf{v}_{\alpha}+\mathbf{v}_{\beta}-\mathbf{d}_{\beta}
\end{array}\right]=\mathbf{0}
$$

- Step 3: Start with the simplex: $\left\{\mathbf{x}_{0}, \mathbf{x}_{1}, \mathbf{x}_{2}\right\}$
- Step 4: Evaluate the simplex vertices into the system $\mathbf{Y}(\mathbf{x})=\mathbf{G}(\mathbf{x})=\mathbf{0}$

$$
\begin{equation*}
\mathbf{Y}(\mathbf{x})=\left[\mathbf{g}\left(\mathbf{x}_{0}\right), \mathbf{g}\left(\mathbf{x}_{1}\right), \mathbf{g}\left(\mathbf{x}_{2}\right)\right]=\mathbf{0} \tag{16}
\end{equation*}
$$

- Step 5: Form the system for the $i$-th iteration

$$
\left[\begin{array}{c}
\mathbf{Y}(\mathbf{x})  \tag{17}\\
1
\end{array}\right]=\left[\begin{array}{c}
\mathbf{G}(\mathbf{x}) \\
1
\end{array}\right] \mu^{(\mathbf{i})}=\left[\begin{array}{l}
\mathbf{0} \\
1
\end{array}\right]
$$

with

$$
\mu^{(\mathbf{i})}=\left[\begin{array}{l}
\mu_{0}^{(i)}  \tag{18}\\
\mu_{1}^{(i)} \\
\mu_{2}^{(i)}
\end{array}\right]=\left[\begin{array}{lll}
\mu_{0}^{(i)}, & \cdots & , \mu_{k}^{(i)}
\end{array}\right]^{T}
$$

and $k \in\{0,1,2\}$

- Step 6 Solve equation (17) for $\mu^{(\mathbf{i})}$
- Step 7: Search the $k$-th element of $\mu^{(\mathbf{i})}$ which is negative. If all the elements of $\mu^{(i)}$ are positive, then go to Step 9, otherwise go to Step 8
- Step 8: Apply the replacement rule to the $\mathbf{x}_{k}$ vertex and return to Step 3

$$
\begin{equation*}
\tilde{\mathbf{x}}_{k}=\mathbf{x}_{k+1}+\mathbf{x}_{k-1}-\mathbf{x}_{k} \tag{19}
\end{equation*}
$$

- Step 9: The solution is found. Compute the solution by

$$
[\mathbf{x}]=\left[\begin{array}{lll}
\mathbf{x}_{0} & \mathbf{x}_{1} & \mathbf{x}_{2} \tag{20}
\end{array}\right] \mu^{(\mathbf{i})}
$$

The above methodology is applied repeatedly in order to compute various operating points.

## VII CASE STUDY

The latch circuit shown in Fig. 5 is a well known three operating points circuit. We are interested in computing these solutions by applying the simplicial methodology presented in the previous section.
This circuit contains two nMOS transistors, two linear resistors ( $R_{1}$ and $R_{2}$ ) and a voltage source ( $V_{D D}$ ).


Figure 5: Example circuit.
The nMOS transistors are described by the two dimensional HC-PWL model. In order to make more legible the HC-PWL formulation for $i_{D S}$, the following notation is introduced

$$
\begin{array}{r}
\delta_{a}^{b}=a\left|v_{D S}-b\right| \\
\phi_{a}^{b}=a\left|v_{G S}-b\right| \\
\gamma_{a}^{b}=a\left|v_{D S}-v_{G S}+b\right| \\
\lambda_{a}^{b, c}=a| | v_{G S}-b\left|+v_{D S}-c\right| \\
-a\left|-v_{G S}+b+\left|v_{D S}-c\right|\right|
\end{array}
$$

And $i_{D S}$ is given by

$$
i_{D S}=\frac{1}{4}\left\{H_{P W L}\right\} \quad \mu A
$$

where

$$
\begin{array}{r}
H_{P W L}=\delta_{32}^{3}+\delta_{125}^{0}+\delta_{-24}^{4}+\delta_{12}^{1}+\delta_{-14}^{2}+\phi_{12}^{4}+\phi_{28}^{3} \\
+\phi_{57}^{2}+\lambda_{1}^{1,1}+\lambda_{-13}^{3,2}+\lambda_{-25}^{4,4}+\lambda_{-26}^{3,1}+\lambda_{9}^{2,1}+\lambda_{-50}^{2,0} \\
\quad+\lambda_{43}^{3,0}+\lambda_{23}^{3,3}+\lambda_{2}^{4,0}+\lambda_{30}^{1,0}+\lambda_{28}^{4,1}+\lambda_{1}^{1,4}+\lambda_{9}^{4,3} \\
\quad+\lambda_{-2}^{4,2}+\lambda_{1}^{1,3}+\lambda_{-1}^{2,3}+\lambda_{1}^{3,4}+\lambda_{-1}^{2,4}+\lambda_{1}^{1,2}+\lambda_{50}^{2,0}
\end{array}
$$

The piecewise linear function $i_{D S}$ is described over an uniformly spaced grid within a finite rectangular region defined by $0 \leq v_{D S} \leq 5$ and $0 \leq v_{G S} \leq 5$ as shown in Fig.6. The grid $v_{D S}-v_{G S}$ is divided into a set of $1 V$ squares and it is subdivided into a simplicial partition.


Figure 6: $i_{D S}$ piecewise-linear curve.
The data points in Fig. 6 follow the Shichman-Hodges model [10]; namely

$$
i_{D S}=\tilde{K}\left[\left(v_{G S}-V_{t}\right) v_{D S}-\frac{1}{2} v_{D S}^{2}\right]
$$

if $v_{D S} \leq v_{G S}-V_{t}$; or
$i_{D S}=\frac{1}{2} \tilde{K}\left(v_{G S}-V_{t}\right)^{2}\left[1+\tilde{\lambda}\left(v_{D S}-v_{G S}+V_{t}\right)\right]$
if $v_{D S}>v_{G S}-V_{t}$, with $\tilde{K}=50 \mu A / V^{2}, V_{t}=1 V$ and $\tilde{\lambda}=0.0 V^{-1}$.
From Fig.5, the following nodal information is collected node $1: i_{D S 1}+\frac{v_{D S 1}-V_{D D}}{R_{1}}=0, \quad v_{D S 1}=v_{G S 2}$
node $2: i_{D S 2}+\frac{v_{D S 2}-V_{D D}}{R_{2}}=0, v_{D S 2}=v_{G S 1}$
Because of $i_{D S 1}=f\left(v_{D S 1}, v_{G S 1}\right)$ and $i_{D S 2}=$ $f\left(v_{D S 2}, v_{G S 2}\right)$, then the above equations can be rewritten into the reduced hybrid system format as

$$
\begin{equation*}
f\left(v_{D S 1}, v_{G S 1}\right)+\left(\frac{1}{R_{1}}\right) v_{D S 1}=\frac{V_{D D}}{R_{1}} \tag{23}
\end{equation*}
$$

$$
\begin{array}{r}
f\left(v_{D S 2}, v_{G S 2}\right)+\left(\frac{1}{R_{2}}\right) v_{D S 2}=\frac{V_{D D}}{R_{2}}  \tag{24}\\
v_{D S 1}-v_{G S 2}=0 \\
v_{D S 2}-v_{G S 1}=0
\end{array}
$$

Notice that equation (23) and equation (24) can be recast as

$$
\begin{align*}
& f\left(e_{1}, e_{2}\right)+\left(\frac{1}{R_{1}}\right) e_{1}=\frac{V_{D D}}{R_{1}}  \tag{25}\\
& f\left(e_{2}, e_{1}\right)+\left(\frac{1}{R_{2}}\right) e_{2}=\frac{V_{D D}}{R_{2}} \tag{26}
\end{align*}
$$

where $e_{1}$ and $e_{2}$ are nodal voltages.
From equation (25) and equation (26), it can be defined the equation system $\mathbf{Y}=\mathbf{0}$ as

$$
\begin{align*}
& y_{1}=i_{D S}\left(e_{1}, e_{2}\right)+\left(\frac{e_{1}-V_{D D}}{R_{1}}\right)=0  \tag{27}\\
& y_{2}=i_{D S}\left(e_{2}, e_{1}\right)+\left(\frac{e_{2}-V_{D D}}{R_{2}}\right)=0 \tag{28}
\end{align*}
$$

Let $R_{1}=R_{2}=30 \mathrm{~K} \Omega$ and $V_{D D}=3.3 \mathrm{~V}$. The solutions of the system $\mathbf{Y}=\mathbf{0}$ are computed as follows.
Firstly, a start simplex is chosen.
Let

$$
x_{0}=\left[\begin{array}{l}
2  \tag{29}\\
4
\end{array}\right], x_{1}=\left[\begin{array}{l}
2 \\
5
\end{array}\right], x_{2}=\left[\begin{array}{l}
3 \\
5
\end{array}\right]
$$

The equation to be solved at the first iteration is

$$
\left[\begin{array}{ccc}
g_{1}\left(\mathbf{x}_{0}\right) & g_{1}\left(\mathbf{x}_{1}\right) & g_{1}\left(\mathbf{x}_{2}\right)  \tag{30}\\
g_{2}\left(\mathbf{x}_{0}\right) & g_{2}\left(\mathbf{x}_{1}\right) & g_{2}\left(\mathbf{x}_{2}\right) \\
1 & 1 & 1
\end{array}\right] \mu^{(\mathbf{i})}=\left[\begin{array}{l}
0 \\
0 \\
1
\end{array}\right]
$$

After substituting it yields

$$
\begin{gather*}
{\left[\begin{array}{c}
\mathbf{G}(\mathbf{x}) \\
1
\end{array}\right] \mu^{(\mathbf{0})}=\left[\begin{array}{l}
0 \\
0 \\
1
\end{array}\right]}  \tag{31}\\
\mathbf{G}(\mathbf{x})=\left[\begin{array}{ccc}
14.37 & 21.67 & 30 \\
5.63 & 9.09 & 18.06
\end{array}\right] \times 10^{-5} \tag{32}
\end{gather*}
$$

The solution is

$$
\mu^{(\mathbf{0})}=\left[\begin{array}{l}
\mu_{0}^{(0)}  \tag{33}\\
\mu_{1}^{(0)} \\
\mu_{2}^{(0)}
\end{array}\right]=\left[\begin{array}{c}
3.22 \\
-2.44 \\
0.22
\end{array}\right]
$$

Since $\mu_{1}^{0}$ is negative, then the vertex $x_{1}$ must be replaced by

$$
\left[\begin{array}{l}
2  \tag{34}\\
4
\end{array}\right]+\left[\begin{array}{l}
3 \\
5
\end{array}\right]-\left[\begin{array}{l}
2 \\
5
\end{array}\right]=\left[\begin{array}{l}
3 \\
4
\end{array}\right]
$$

The new simplex is defined by

$$
\left[\begin{array}{l}
2 \\
4
\end{array}\right],\left[\begin{array}{l}
3 \\
4
\end{array}\right],\left[\begin{array}{l}
3 \\
5
\end{array}\right]
$$

In this simplex, the new equation to solve is

$$
\begin{gather*}
{\left[\begin{array}{c}
\mathbf{G}(\mathbf{x}) \\
1
\end{array}\right] \mu^{(\mathbf{1})}=\left[\begin{array}{l}
0 \\
0 \\
1
\end{array}\right]}  \tag{35}\\
\mathbf{G}(\mathbf{x})=\left[\begin{array}{ccc}
14.37 & 20.5 & 30 \\
5.63 & 14.6 & 18.06
\end{array}\right] \times 10^{-5} \tag{36}
\end{gather*}
$$

The solution is

$$
\mu^{(\mathbf{1})}=\left[\begin{array}{l}
\mu_{0}^{(1)}  \tag{37}\\
\mu_{1}^{(1)} \\
\mu_{2}^{(1)}
\end{array}\right]=\left[\begin{array}{c}
1.065 \\
1.405 \\
-1.47
\end{array}\right]
$$

And the vertex to be replaced is $x_{2}$ by $[23]^{T}$. The new simplex is then defined by

$$
\left[\begin{array}{l}
2 \\
4
\end{array}\right],\left[\begin{array}{l}
3 \\
4
\end{array}\right],\left[\begin{array}{l}
2 \\
3
\end{array}\right]
$$

The results in the third, fourth, and fifth iteration are

$$
\mu^{(\mathbf{2})}=\left[\begin{array}{l}
\mu_{0}^{(2)}  \tag{38}\\
\mu_{1}^{(2)} \\
\mu_{2}^{(2)}
\end{array}\right]=\left[\begin{array}{c}
-1.709 \\
0.295 \\
2.414
\end{array}\right]
$$

with the following simplex defined in

$$
\begin{gather*}
{\left[\begin{array}{l}
3 \\
3
\end{array}\right],\left[\begin{array}{l}
3 \\
4
\end{array}\right],\left[\begin{array}{l}
2 \\
3
\end{array}\right]} \\
\mu^{(\mathbf{3})}=\left[\begin{array}{l}
\mu_{0}^{(3)} \\
\mu_{1}^{(3)} \\
\mu_{2}^{(3)}
\end{array}\right]=\left[\begin{array}{c}
0.93 \\
-0.855 \\
0.918
\end{array}\right] \tag{39}
\end{gather*}
$$

and the new simplex defined in

$$
\begin{gather*}
{\left[\begin{array}{l}
3 \\
3
\end{array}\right],\left[\begin{array}{l}
2 \\
2
\end{array}\right],\left[\begin{array}{l}
2 \\
3
\end{array}\right]} \\
\mu^{(4)}=\left[\begin{array}{l}
\mu_{0}^{(4)} \\
\mu_{1}^{(4)} \\
\mu_{2}^{(4)}
\end{array}\right]=\left[\begin{array}{c}
0.099 \\
0.9 \\
0
\end{array}\right] \tag{40}
\end{gather*}
$$

Since $\mu^{\mathbf{4}}>\mathbf{0}$, there is not following simplex to jump, then the solution is computed by

$$
\left[\begin{array}{l}
e_{1}  \tag{41}\\
e_{2}
\end{array}\right]=\left[\begin{array}{lll}
3 & 2 & 2 \\
3 & 2 & 3
\end{array}\right] \mu^{(4)}=\left[\begin{array}{l}
2.099 \\
2.099
\end{array}\right]
$$

After applying the same algorithm into two different start simplices, the following solutions can be computed

$$
\begin{align*}
& {\left[\begin{array}{l}
e_{1} \\
e_{2}
\end{array}\right]=\left[\begin{array}{lll}
1 & 0 & 1 \\
3 & 3 & 4
\end{array}\right] \mu^{(4)}=\left[\begin{array}{c}
0.857 \\
3.3
\end{array}\right]}  \tag{42}\\
& {\left[\begin{array}{l}
e_{1} \\
e_{2}
\end{array}\right]=\left[\begin{array}{lll}
3 & 3 & 4 \\
0 & 1 & 1
\end{array}\right] \mu^{(\mathbf{5})}=\left[\begin{array}{c}
3.3 \\
0.857
\end{array}\right]} \tag{43}
\end{align*}
$$

Notice that the three computed solutions can be substituted into the eq.(25) and eq.(26) and the condition $\mathbf{Y}=\mathbf{0}$ is fullfil. The paths followed by the algorithm to obtain the solutions are depicted in Fig.7.


Figure 7: Path solutions into the simplicial partition.

## VIII Conclusions

A methodology for finding operating points in networks containing MOS transistors was proposed. Such methodology is based into the Kuh-Chien algorithm and it is able to handle equation systems which involve the HC-PWL description. Because the algorithm is able to compute only one solution, in multiple operating point systems it is necessary to apply it as many times as solutions exists. It presents two important challenges, the first consists in determining previously to the analysis, the maximum number of existing solutions of the system. The other one consists in determining the optimal starting point for running the Kuh-Chien algorithm. The analysis of both problems promise interesting results in this topic under investigation

## IX Aknowledgment

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